

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Amendment/Response Transmittal

In re application of:

William J. Armstrong, et al.

Group Art Unit:

2126

Serial No.:

09/838,057

Examiner:

V. Nguyen

Filed:

April 19, 2001

For: METHOD AND APPARATUS FOR

ALLOCATING PROCESSOR RESOURCES

IN A LOGICALLY PARTITIONED

COMPUTER SYSTEM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing is an Amendment/Response in the above-identified Application.

X No additional fee is required.

The filing fee has been calculated as follows:

	Claims Remaining After Amendment	Highest Number Previously Paid For	Present Extra
Total	16	- 20	0
Independent	3	- 3	0
First Presentation of Multiple Dependent Claim			

Other Than Small Entity

Rate	Additional Fee	
	•	
x \$18.00=	0.00	
x \$88.00=	0.00	
\$300.00	0.00	
TOTAL	0.00	

CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8(a)

I hereby certify that the enclosed or attached correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 28, 2004

Roy W. Truelson

Amendment/Response Transmittal
Attorney Docket No.: ROC920000252US1

	onth extension of time to respond to the Office Action dated t 09-0465 in the amount of \$ A duplicate copy of this
above. A duplicate copy of this sheet is e The Commissioner is hereby authorized t	to charge payment of the following fees associated with this at to Deposit Account 09-0465. A duplicate copy of this sheet is d under 37 C.F.R. §1.16.
	Respectfully submitted,
October 28, 2004	WILLIAM J. ARMSTRONG, et al.
89-6256 voice/fax	Roy W. Truelson Registration No : 34 265
	WILLIAM J. ARMSTRONG, et al.



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: William J. Armstrong, et al. :

Date: October 28, 2004

Group Art Unit:

2126

IBM Corporation

Examiner:

V. Nguyen

Intellectual Property Law

Serial No.:

09/838,057

Dept. 917, Bldg. 006-1

Filed:

April 19, 2001

3605 Highway 52 North

Title: METHOD AND APPARATUS FOR

ALLOCATING PROCESSOR RESOURCES IN A LOGICALLY

PARTITIONED COMPUTER SYSTEM

Rochester, MN 55901

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October 28, 2004

Box 1450, Alexandria, VA 22313-1450, on

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

(Date-of Deposit)

Sir:

AMENDMENT

This paper is being filed in response to the office action dated July 29, 2004. Please amend the above identified application as follows.

Docket No.:

ROC920000252US1

Serial No.:

09/838,057